

AUG 18 2005
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Certificate

AUG 22 2005

of Correction**FACSIMILE COVER SHEET**

To: Commissioner for Patents Mail Stop Post Issuance	Total Pages Sent: 5 (including cover sheet)
Facsimile Number: 571-273-8300	Transmission Date: August 18, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

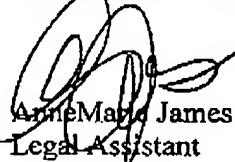
Applicant: Chen	Docket No.: TSM03-0323
Serial No: 10/676,703	Patent No.: 6,924,531 B2
Date Filed: October 1, 2003	Date Issued: August 2, 2005
Title: LDMOS Device with Isolation Guard Rings	

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 571-273-8300 on the date shown above:

- Certification of Facsimile Transmission (1 page)
- Request for Issuance of Certificate of Correction (1 original and 1 copy)
- Certificate of Correction (1 original and 1 copy)

Respectfully submitted,



Anne Marie James
Legal Assistant

Confirmation Respectfully Requested

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patentee: Chen, et al.

Docket No.: TSM03-0323

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Filed: October 1, 2003

Examiner: Remmon R. Denton

Serial No.: 10/676,703

Art Unit: 2826

AUG 18 2005

Patent No.: 6,924,531 B2

Issue Date: August 2, 2005

For: LDMOS Device with Isolation Guard Rings

Certificate of Corrections Branch
Commissioner for PatentsP.O. Box 1450
Alexandria, VA 22313-1450**Request for Issuance of Certificate of Correction**
Pursuant to 35 U.S.C. § 254 and 37 C.F.R. § 1.322

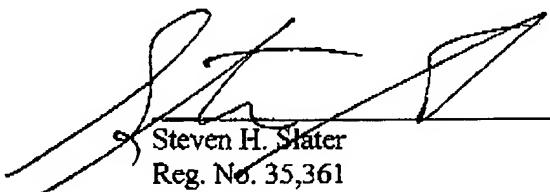
Dear Sir:

Upon review of the above-identified issued patent, Patentee notes errors in the claims that should be corrected as shown on Form PTO/SB/44, attached hereto in duplicate.

Some of the mistakes noted on the attached Certificate of Correction were not incurred through the fault of the Patent Office. Accordingly, please charge the required fee of \$100 for this Request for Certificate of Correction, and any other required fees to Deposit Account 50-1065.

Patentee respectfully solicits the issuance of the requested Certificate of Correction.

Respectfully submitted,


Steven H. Slater
Reg. No. 35,361August 18, 2005

Date

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COPY**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Patentee: Chen, et al. Docket No.: TSM03-0323
Filed: October 1, 2003 Examiner: Remmon R. Forde
Serial No.: 10/676,703 Art Unit: 2826
Patent No.: 6,924,531 B2 Issue Date: August 2, 2005
For: LDMOS Device with Isolation Guard Rings

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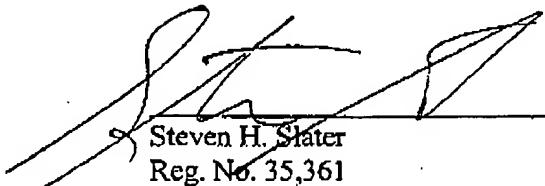
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PTO/SB/44 (04-05)

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**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**Page 1 of 1

PATENT NO. : 6,924,531 B2

APPLICATION NO. : 10/676,703

ISSUE DATE : 8/2/2005

INVENTOR(S) : Chen, et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

At Column 7, line 9: delete "N well," insert -- N-well --.

At Column 9, line 15: delete "fist," insert -- first --.

MAILING ADDRESS OF SENDER:

Slater & Matsil, L.L.P.
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Suite 1000
Dallas, Texas 75252

PATENT NO: 6,924,531 B2

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